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Substitute for form 1449A/PTO

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Complete if Known

Application Number

Filing Date

June 10, 2005

First Named Inventor

Anne Gattiker et al.

Art Unit

Examiner Name

Attorney Docket Number

BUR920020068US1

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
		US-6,715,090 B1	03-30-2004	Totsuka et al.	
		US-6,330,697 B1	12-11-2001	Clinton et al.	
		US-6,301,168 B1	10-9-2001	Crocker	
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FOREIGN PATENT DOCUMENTS

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Substitute for form 1449B/PTO		Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)		Application Number	10/539247
		Filing Date	June 10, 2005
		First Named Inventor	Anne Gattiker
		Group Art Unit	
		Examiner Name	
Sheet 2 of 2	Attorney Docket Number	BUR920020068US1	

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		FERRO, E. E. et al., "Quiescent Power Supply Current Test for CMOS Integrated Circuits Using Non-Complimentary Pass Gate Logic," IBM Technical Disclosure Bulletin, Vol. 35, No. 1B, June 1992, pp. 354-358.	
		SATO, Y. et al., "An Evaluation of Defect-Oriented Test: WELL-Controlled Low Voltage Test," ITC International Test Conference, IEEE, 2001, pp. 1059-1067.	

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